



PATENT 8026-1004

IN THE U.S. PATENT AND TRADEMARK OFFICE

In re application of

Koki HIRASAWA et al.

Conf. 2347

Application No. 09/923,394 Group 2826

Filed August 8, 2001

Examiner F. Erdem

LEAD FRAME, SEMICONDUCTOR DEVICE PRODUCED BY USING THE SAME AND METHOD OF PRODUCING THE SEMICONDUCTOR DEVICE

SUPPLEMENAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In compliance with Rules 1.97 and 1.98, and in fulfillment of the duty of disclosure under Rule 1.56, the accompanying document, a copy of which is attached to this statement, is made of record on the enclosed Form PTO-1449.

A concise explanation of the relevance of this item is that this reference was cited by the Korean Patent Office in an Official Action. A copy of the Korean Official Action and Japanese translation in which it was cited is attached hereto. An English translation is also attached hereto.

Under the provisions of 37 CFR 1.97(e), the undersigned hereby certifies that each item of information contained in this Supplemental Information Disclosure Statement was first cited in any communication from a foreign Patent Of-

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fice in a counterpart foreign application not more than three months prior to the filing of this Statement.

Respectfully submitted,

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July 16, 2004

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	IN AN APPLICATION	ON	Applicant: Koki HIRASAWA	et al.			
(Use several sheets if necessary)			Filing Date: August 8, 2001		Group Art Unit: 2826		
		U.S. PATEN	T DOCUMENTS				<u> </u>
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DEMARK							
		FOREIGN PAT	ENT DOCUMENTS				
Examiner Document Number		Date	Country	Clas	ss Subclas	ass Translation	
Initial	JP 59-198782	11/10/1984	JAPAN			Yes	No
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	 ER:		DATE CONSIDERED				

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.

* Abstract provided for the Examiner's convenience